



SPECIFICATION FOR LCM+CTP Module

MODULE No:	KD043WVFPA034-C030A
CUSTOMER:	

STARTEK	INITIAL	DATE
PREPARED BY		
CHECKED BY		
APPROVED BY		

CUSTOMER	INITIAL	DATE
APPROVED BY		

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常备库存 Stock For Sale	长期供货 Long Time supply	支持小量 NO MOQ	品种齐全 In Full Range	

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1.Basic Specifications

* Description

This is a color active matrix TFT (Thin Film Transistor) LCD (liquid crystal display) that uses amorphous silicon TFT as a switching device. This module is composed of a Transmissive type TFT-LCD Panel, driver circuit, capacitance touch panel,back-light unit. The resolution of a 4.3 " TFT-LCD contains 800x480 pixels, and can display up to 16.7M colors.

1.1 TFT Features

General Information Items	Specification	Unit	Note
	Main Panel		
Display area(AA)	95.04(H)*53.86(V) (4.3inch)	mm	
Driver element	TFT active matrix	-	
Display colors	65K/262K16.7M	colors	
Number of pixels	800(RGB)*480	dots	
Pixel arrangement	RGB vertical stripe	-	
Pixel pitch	0.1188(H)*0.1122(V)	mm	
Viewing angle	Free	o'clock	
Controller IC	HX8246+HX8664-B	-	
Display mode	Transmissive /Normally Black	-	
LCM Interface	16/18/24BIT RGB	-	
Operating temperature	-30~+85	°C	
Storage temperature	-30~+85	°C	
Module bonding technology	Use Tape bonding between LCM and CTP	-	

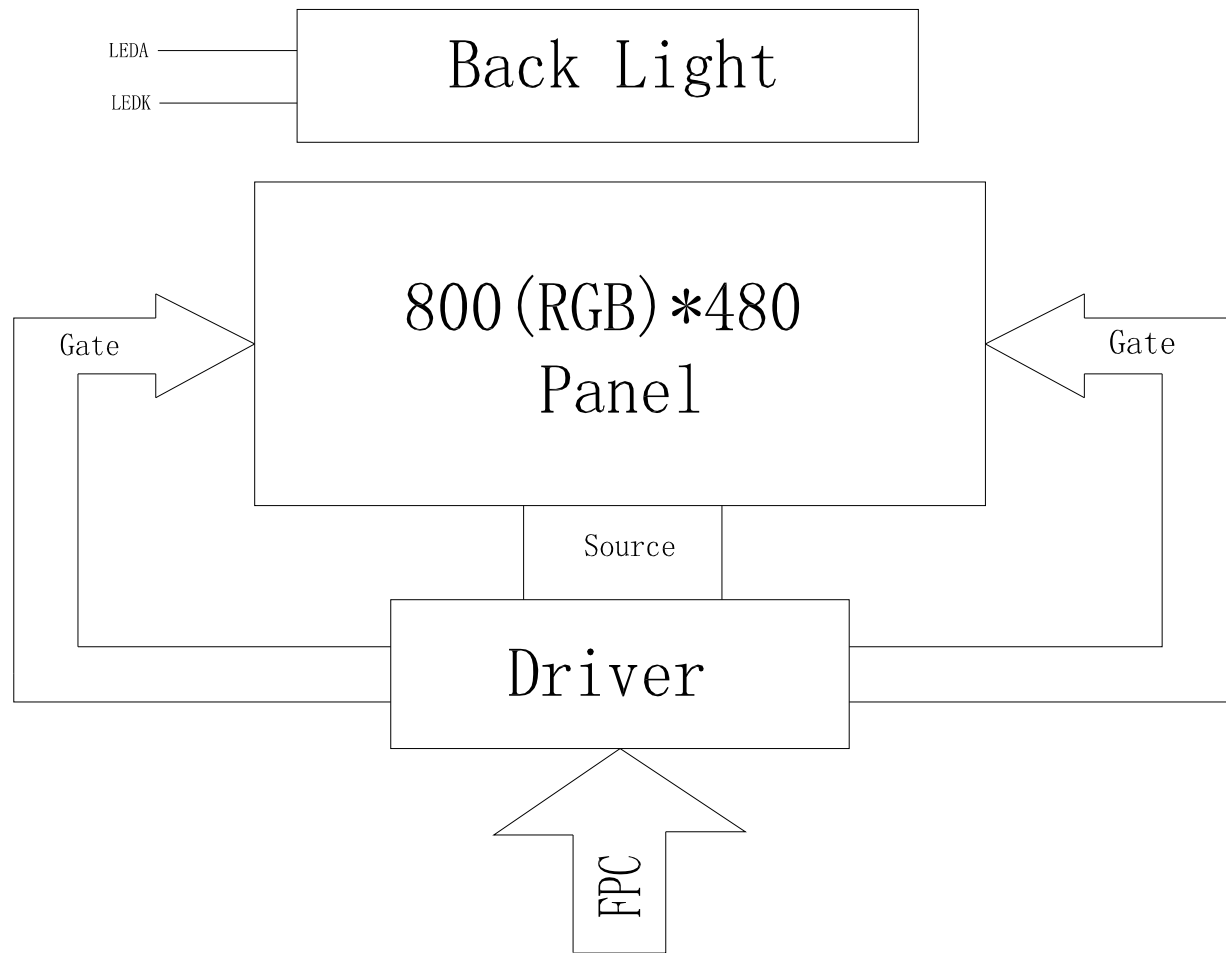
1.2 CTP Features

General Information Items	Specification	Unit	Note
	Main Panel		
Resolution	800(H)*480(V)	-	
Structure	G+G	-	
Controller IC	FT5436DQQ	-	
Interface	I2C	-	
Slave Adress	0x38(7bit)/8bt:0x70(Write) 0x71(Read)	-	
Touch mode	Five points and Gestures	-	-

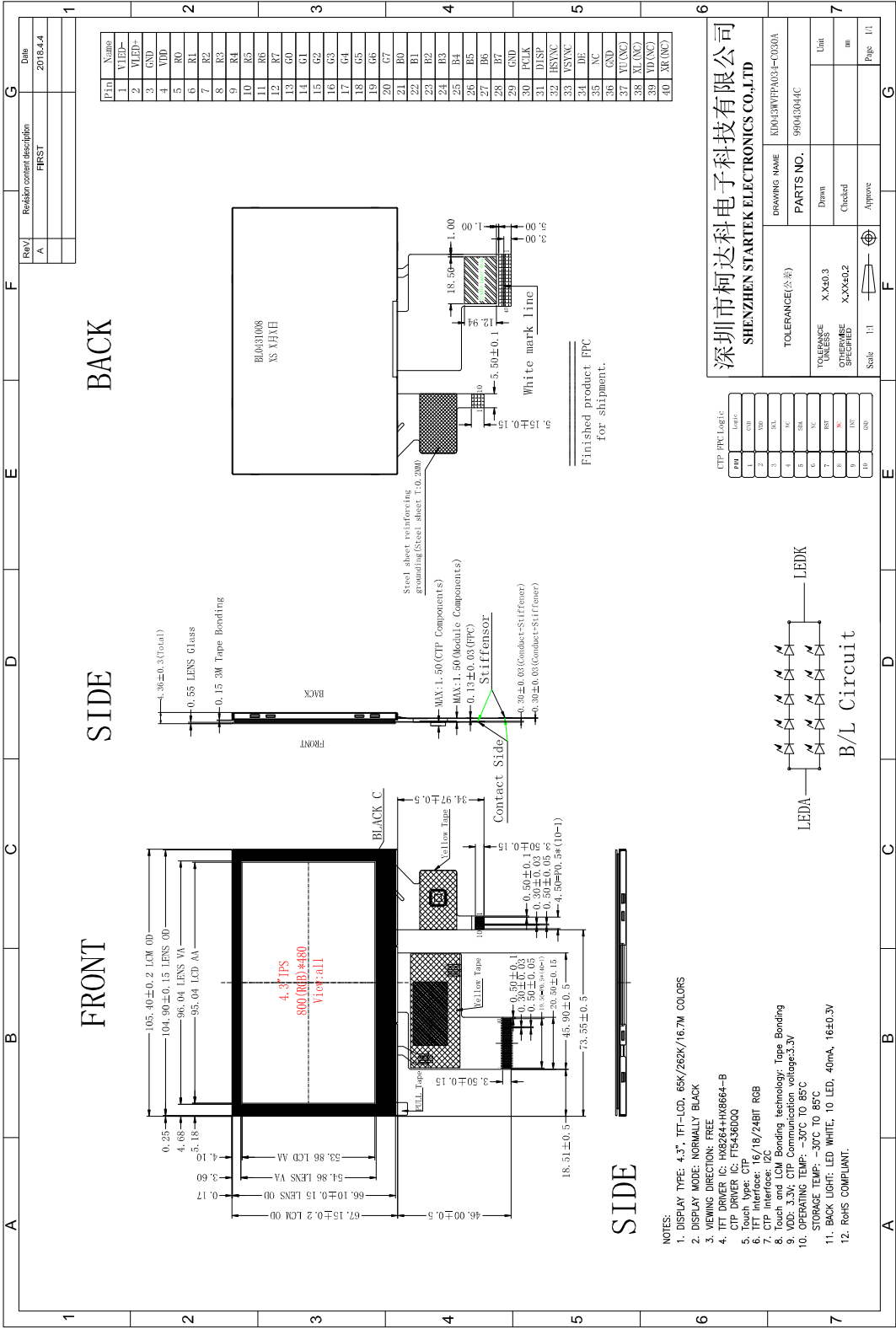
1.3 Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal(H)	-	105.40	-	mm	
	Vertical(V)	-	67.15	-	mm	
	Depth(D)	-	4.36	-	mm	
Weight		-	TBD	-	g	

2. Block Diagram



3. Outline dimension



4. Input terminal Pin Assignment

4.1 TFT PIN Define

NO.	SYMBOL	DISCRIPTION	I/O
1	VLED-	Cathode pin OF backlight	P
2	VLED+	Anode pin of backlight	P
3	GND	Ground.	P
4	VDD	Supply voltage(3.3V).	P
5	R0	Red data input.	I
6	R1	Red data input.	I
7	R2	Red data input.	I
8	R3	Red data input.	I
9	R4	Red data input.	I
10	R5	Red data input.	I
11	R6	Red data input.	I
12	R7	Red data input.	I
13	G0	Green data input.	I
14	G1	Green data input.	I
15	G2	Green data input.	I
16	G3	Green data input.	I
17	G4	Green data input.	I
18	G5	Green data input.	I
19	G6	Green data input.	I
20	G7	Green data input.	I
21	B0	Blue data input.	I
22	B1	Blue data input.	I
23	B2	Blue data input.	I

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24	B3	Blue data input.	I
25	B4	Blue data input.	I
26	B5	Blue data input.	I
27	B6	Blue data input.	I
28	B7	Blue data input.	I
29	GND	Ground.	P
30	PCLK	Clock signal. Latching data at the rising edge	I
31	DISP	Standby setting for testing, it should be connected to VDDIO in normal operation mode. If connected to GND, the IC is in standby mode.	I
32	HSYNC	Horizontal Sync input. Negative polarity.	I
33	VSYNC	Vertical Sync input. Negative polarity.	I
34	DE	Data input Enable. Active High to enable the data input Bus under "DE Mode".	I
35	NC		
36	GND	Ground.	P
37	XR(NC)	Touch panel Right Glass Terminal	A/D
38	YD(NC)	Touch panel Bottom Film Terminal	A/D
39	XL(NC)	Touch panel LIFT Glass Terminal	A/D
40	YU(NC)	Touch panel Top Film Terminal	A/D



4.2 CTP PIN Define

NO.	SYMBOL	DISCRIPTION	I/O
1	GND	Ground.	P
2	VDD	Supply voltage.	P
3	SCL	I2C clock input.	P
4	NC	NC	
5	SDA	I2C data input and output	I/O
6	NC	NC	
7	RST	External Reset, Low is active.	I
8	NC	NC	
9	INT	External interrupt to the host.	I
10	GND	Ground.	P

5. LCD Optical Characteristics

5.1 Optical specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit.	Note
Contrast Ratio		CR	$\Theta=0$ Normal viewing angle	640	800	--		(1)(2)
Response time	Rising	T_R+T_F		--	30	40	msec	
	Falling			--				(1)(3)
Uniformity		S(%)		45	50	--	%	
Color Filter Chromaticity	White	W_X		0.293	0.333	0.373		(1)(4) CF glass
		W_Y		0.330	0.370	0.410		
	Red	R_X		0.5639	0.6039	0.6439		
		R_Y		0.3143	0.3543	0.3943		
	Green	G_X		0.324	0.364	0.404		
		G_Y		0.5494	0.5894	0.6294		
	Blue	B_X		0.1107	0.1507	0.1907		
		B_Y		0.0639	0.1039	0.1439		
Viewing angle	Hor.	Θ_L	CR>10	--	80	--		
		Θ_R		--	80	--		
	Ver.	Θ_U		--	80	--		
		Θ_D		--	80	--		
		Option View Direction		Free				

5.2 Measuring Condition

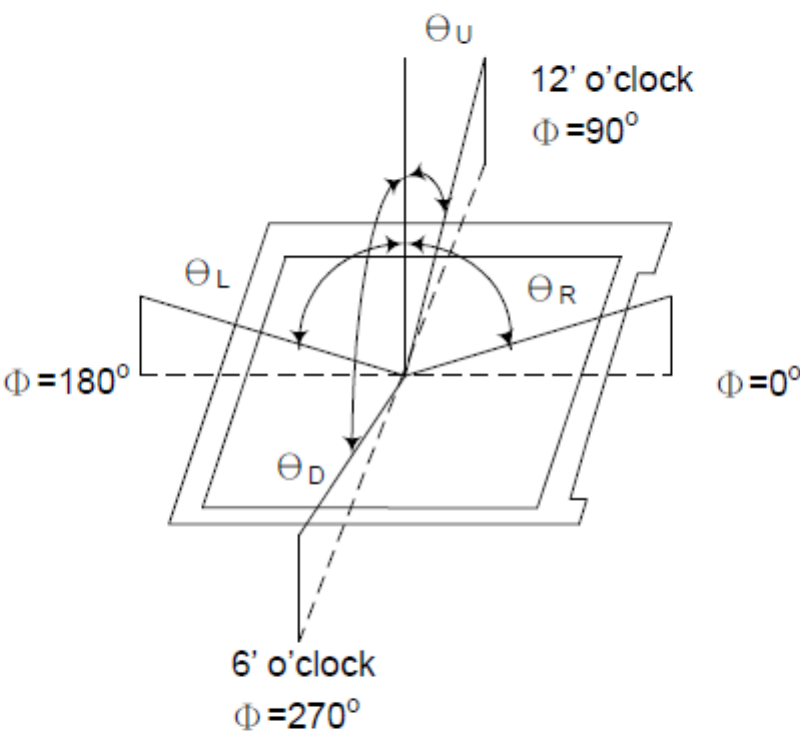
- Measuring surrounding: dark room
- Ambient temperature: $25 \pm 2^\circ\text{C}$
- 15min. warm-up time.

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5.3 Measuring Equipment

FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.

Note (1) Definition of Viewing Angle:



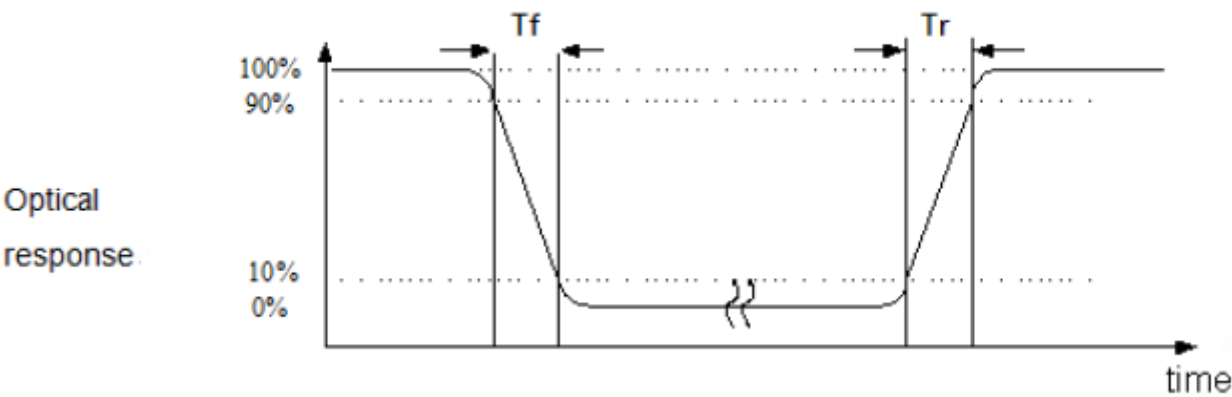
Note (2) Definition of Contrast Ratio (CR):
measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

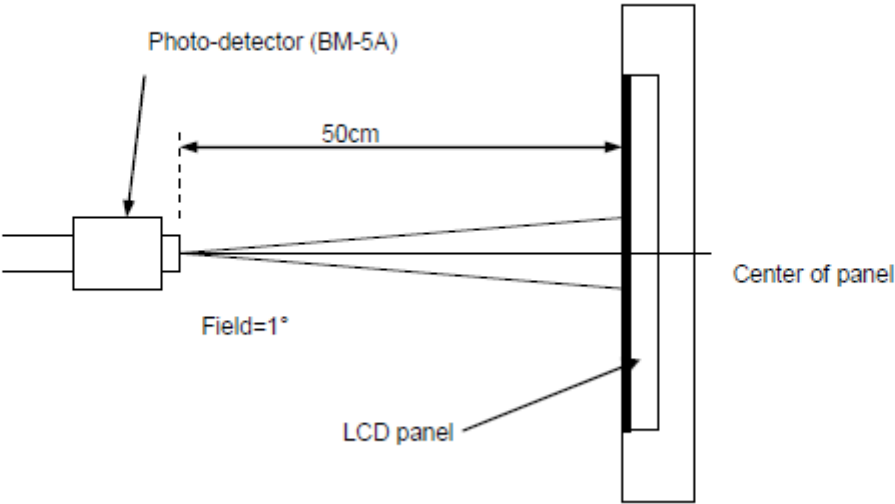
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Note (3) Definition of Response Time: Sum of T_R and T_F



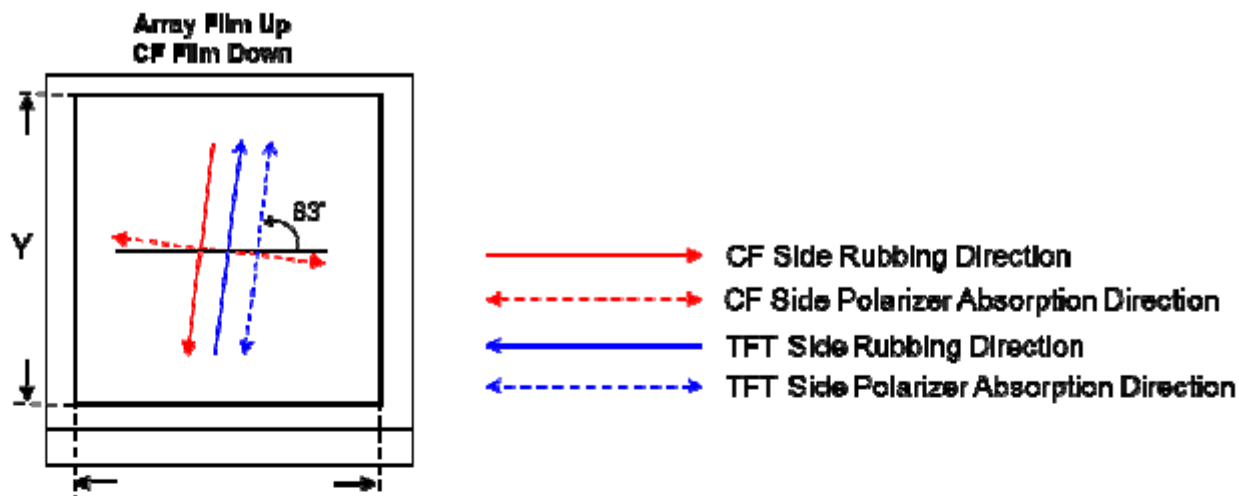
Note (4) Definition of optical measurement setup



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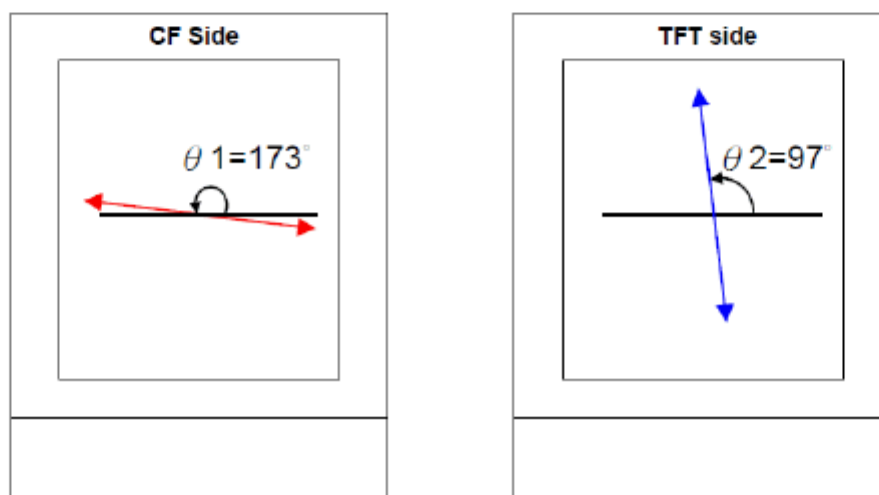
Note (5) Rubbing Direction & Polarizer Absorption Direction



Item	Specifications	Unit	Note
Rubbing Direction	83° (TFT) / 263° (CF)	degree	1-domain IPS-pro
Absorption axis of Polarizer	83° (TFT) / 173° (CF)	degree	Array Film Up CF Film Down

CF side polarizing absorption angle $\theta 1=173^\circ$ (Protective film on top, glue layer face down)

TFT side polarizing absorption angle $\theta 2=97^\circ$ (Protective film on top, glue layer face down)



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6. Electrical Characteristics

6.1 Absolute Maximum Rating

Characteristics	Symbol	Min.	Max.	Unit	Note
Digital Supply Voltage	VDD	-0.5	3.96	V	Note1
Operating temperature	T _{OP}	-30	+85	°C	
Storage temperature	T _{ST}	-30	+85	°C	

NOTE1: If the absolute maximum rating of even is one of the above parameters is exceeded even momentarily, the quality of the product may be degraded. Absolute maximum ratings, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the range of the absolute maximum ratings.

6.2 DC Electrical Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Digital Supply Voltage	VDD	2.7	3.3	3.6	V	
Normal mode Current	IDD	--	120	--	mA	
Level input voltage	V _{IH}	0.7VDD		VDD	V	
	V _{IL}	GND		0.3 VDD	V	
Level output voltage	V _{OH}	VDD-0.4		--	V	
	V _{OL}	GND		GND+0.4	V	

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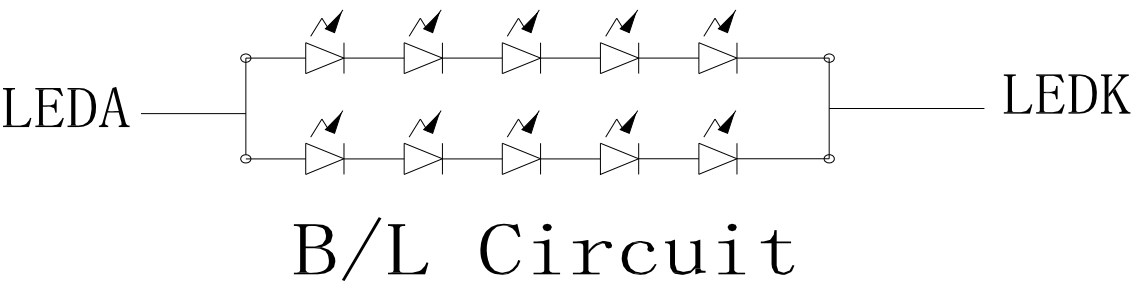
6.3 LED Backlight Characteristics

The back-light system is edge-lighting type with 10 chips LED

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Forward Current	I _F	30	40	--	mA	
Forward Voltage	V _F	--	16	--	V	
LCM Luminance	LV	409	459	--	cd/m2	Note3
LED life time	Hr	--	50000	--	Hour	Note1,2
Uniformity	Avg	80	--	--	%	Note3

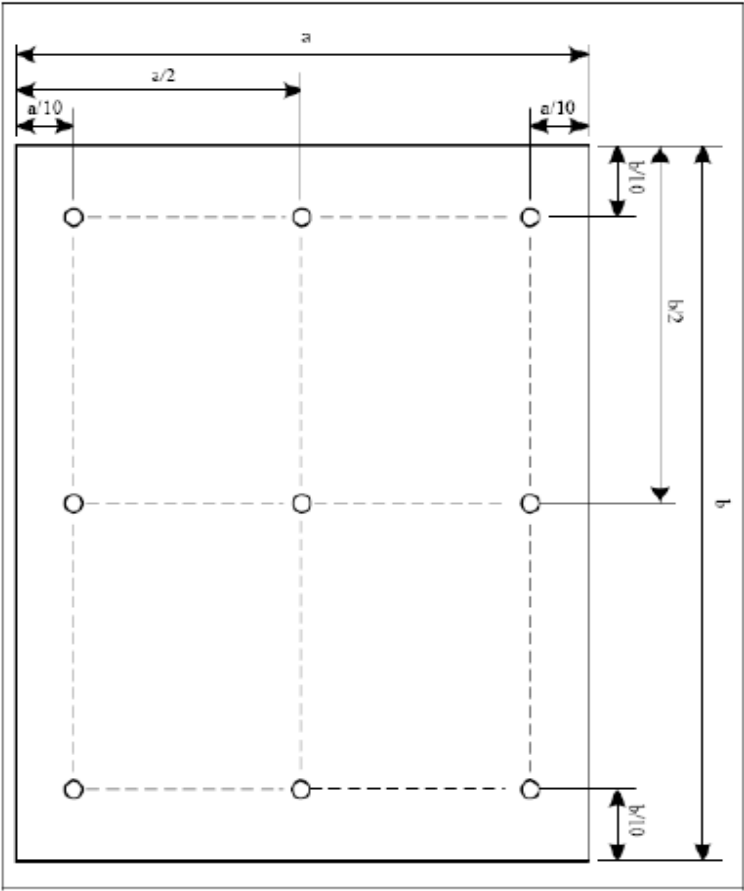
Note1: LED life time (Hr) can be defined as the time in which it continues to operate under the condition:
Ta=25±3 °C, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note 2: The “LED life time” is defined as the module brightness decrease to 50% original brightness at
Ta=25℃ and IL=40mA. The LED lifetime could be decreased if operating IL is larger than 40mA. The constant current driving method is suggested.





Note (3) Luminance Uniformity of these 9 points is defined as below:



Uniformity = $\frac{\text{minimum luminance in 9 points (1-9)}}{\text{maximum luminance in 9 points (1-9)}}$

Luminance = $\frac{\text{Total Luminance of 9 points}}{9}$

7. AC Characteristics

7.1 AC Timing characteristics

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
HS setup time	T_{hst}	8	-	-	ns
HS hold time	T_{hhd}	8	-	-	ns
VS setup time	T_{vst}	8	-	-	ns
VS hold time	T_{vhd}	8	-	-	ns
Data setup time	T_{dsu}	8	-	-	ns
Data hold time	T_{dhd}	8	-	-	ns
DE setup time	T_{esu}	8	-	-	ns
DE hold time	T_{ehd}	8	-	-	ns
VDD Power On Slew rate	T_{POR}	-	-	20	ms
RSTB pulse width	T_{Rst}	10	-	-	us
CLKIN cycle time	T_{cph}	20	-	-	ns
CLKIN pulse duty	T_{owh}	40	50	60	%
Output stable time	T_{sst}	-	-	6	us

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLKIN Frequency	Fclk	-	40	50	MHz	VDD=3.0V~3.6V
CLKIN Cycle Time	Tclk	20	25	-	ns	-
CLKIN Pulse Duty	Tcwh	40	50	60	%	Tclk
Time from HSD to Source Output	Thso		64		CLKIN	-
Time from HSD to LD	Thld		64		CLKIN	-
Time from HSD to STV	Thstv		2		CLKIN	-
Time from HSD to CKV	Thckv		20		CLKIN	-
Time from HSD to OEV	Thoev		4		CLKIN	-
LD Pulse Width	Twld		10		CLKIN	-
CKV Pulse Width	Twckv		66		CLKIN	-
OEV Pulse Width	Twoev		74		CLKIN	-

Table 12. 1: Parallel 24-bit RGB mode

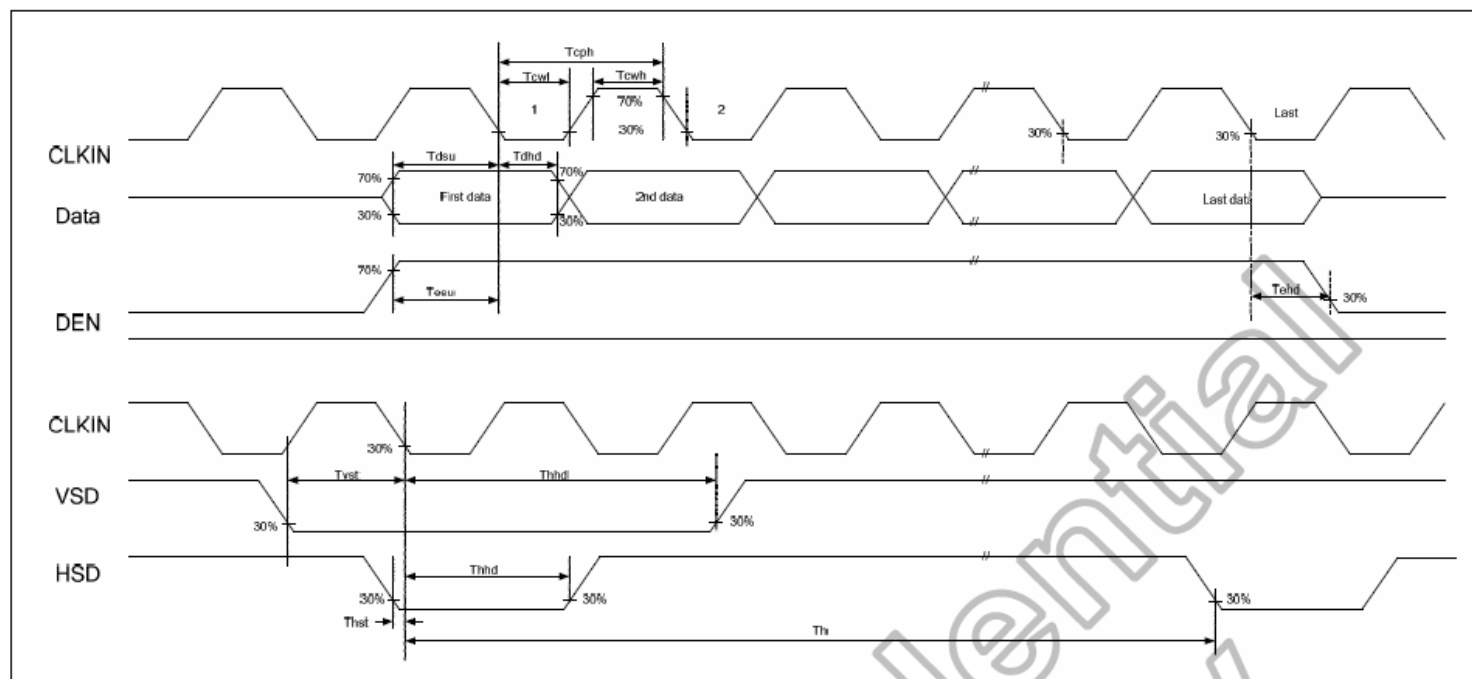


Figure 12. 1: Input Clock and Data Timing Diagram

7.2 Data input format

● Horizontal timing

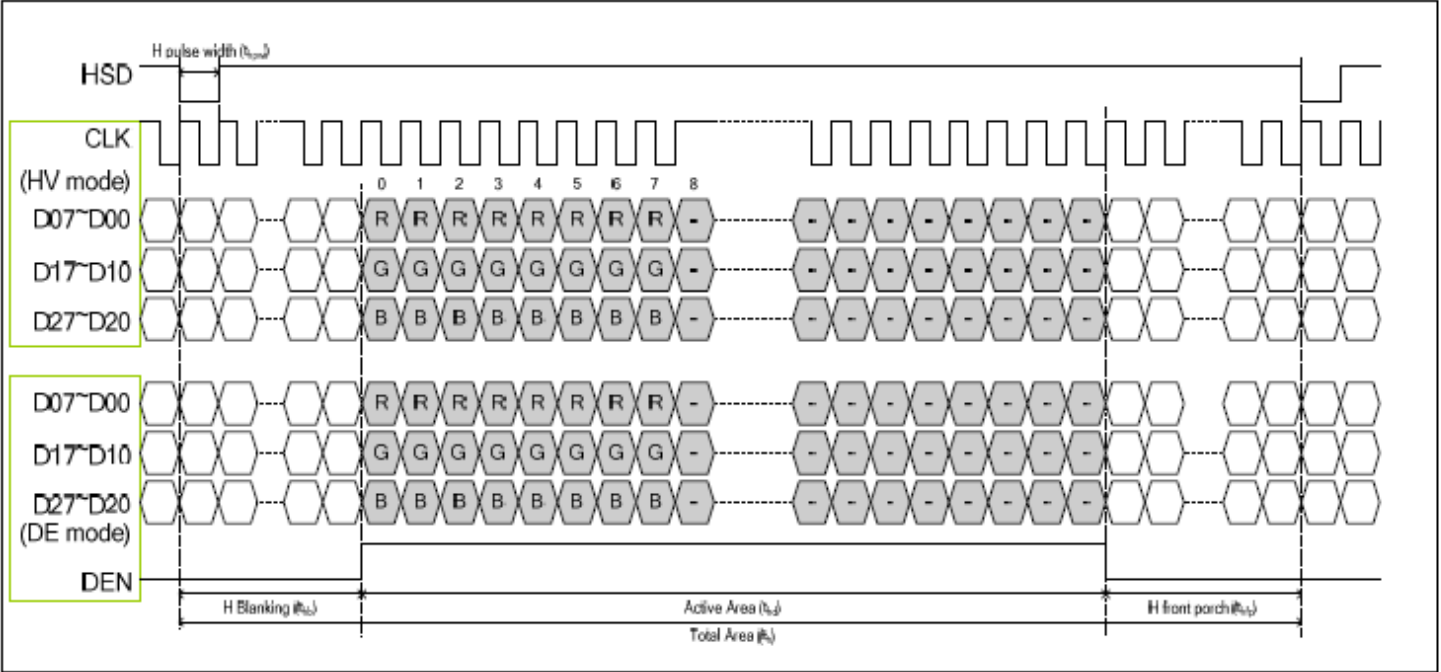


Figure 11. 1: Horizontal Input Timing Diagram

● Vertical timing

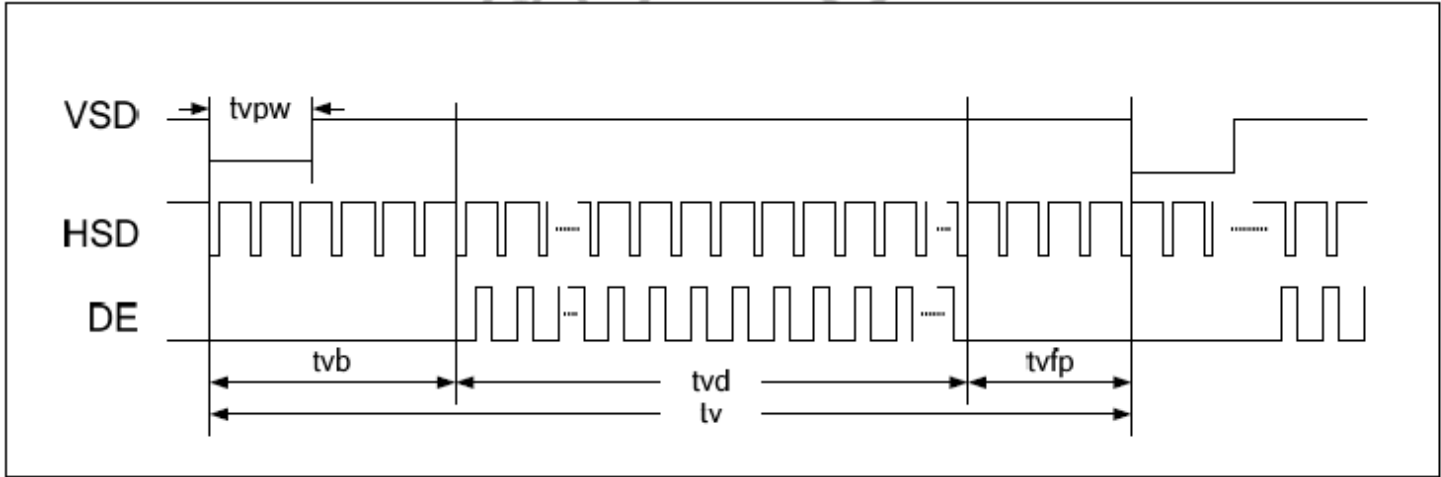


Figure 11. 2: Vertical Input Timing Diagram

● **Horizontal timing**

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Horizontal Display Area	thd	800			DCLK
DCLK frequency	fclk	-	30	50	MHz
One Horizontal Line	th	862	1056	1200	DCLK
HS pulse width	thpw	1	-	40	DCLK
HS Back Porch (Blanking)	thb	46			DCLK
HS Front Porch	thfp	16	210	354	DCLK
DE mode Blanking	th-thd	85	256	400	DCLK

● **Vertical timing**

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Vertical Display Area	tvd	480			T _H
VS period time	tv	513	525	650	T _H
VS pulse width	tvpw	3	-	20	T _H
VS Back Porch (Blanking)	tvb	23			T _H
VS Front Porch	tvfp	7	22	147	T _H
DE mode Blanking	tv-tvd	30	45	170	T _H

8. CTP Specification

8.1 Electrical Characteristics

8.1.1 Absolute Maximum Rating

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	VDD	2.7	3.6	V	
Operating temperature	T _{OP}	-30	+85	°C	
Storage temperature	T _{ST}	-30	+85	°C	

8.1.2 DC Electrical Characteristics (Ta=25°C)

Item	Min.	Typ.	Max.	Unit	Note
Power Supply Voltage/VDD	2.7	3.3	3.47	V	
Normal mode operating current	--	11	--	mA	
Sleep mode operating current	--	42	--	uA	
Monitor mode operating current	--	0.43	--	mA	
Digital Input low voltage/VIL	-0.3	--	0.3*VDD	V	
Digital Input high voltage/VIH	0.7*VDD	--	VDD	V	
Digital Output low voltage/VOL	--	--	0.3*VDD	V	
Digital Output high voltage/VOH	0.7*VDD	--	--	V	



8.1.3 AC Characteristics

AC Characteristics of Oscillators

Item	Symbol	Unit	Test Condition	Min.	Typ.	Max.	Note
OSC clock 1	fosc1	MHz	VDD3 = 2.8V; Ta=25°C	49	50	51	

8.2 POWER ON/Reset Sequence

Reset should be pulled down to be low before powering on and powering down. I2C shouldn't be used by other devices during Reset time after VDD powering on (Trtp). INT signal will be sent to the host after initia-lizing all parameters and then start to report points to the host. If Power is down, the voltage of supply must be below 0.3V and Tpdtr is more than 1ms.

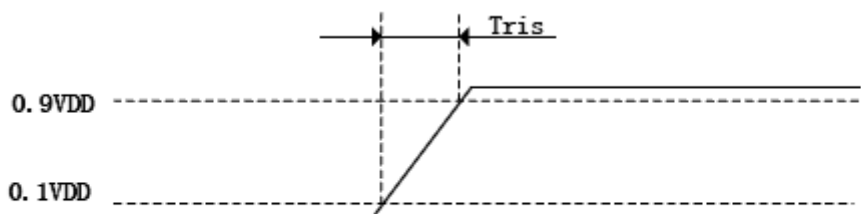


Figure 3-3 Power on time

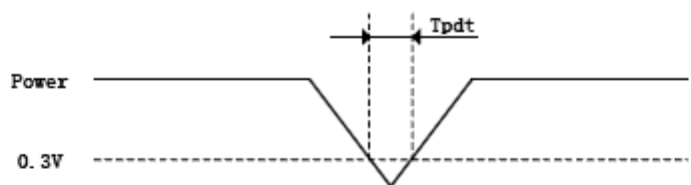


Figure 3-4 Power Cycle requirement

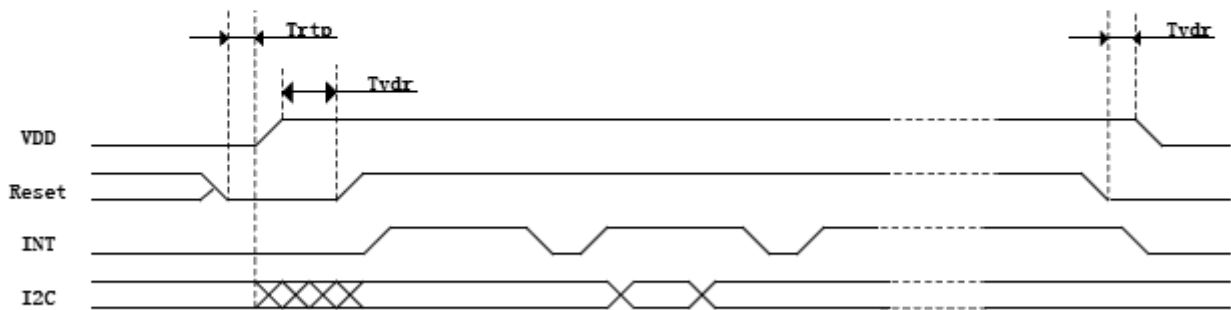


Figure 3-5 Power on Sequence

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Reset time must be enough to guarantee reliable reset, the time of starting to report point after resetting approach to the time of starting to report point after powering on.

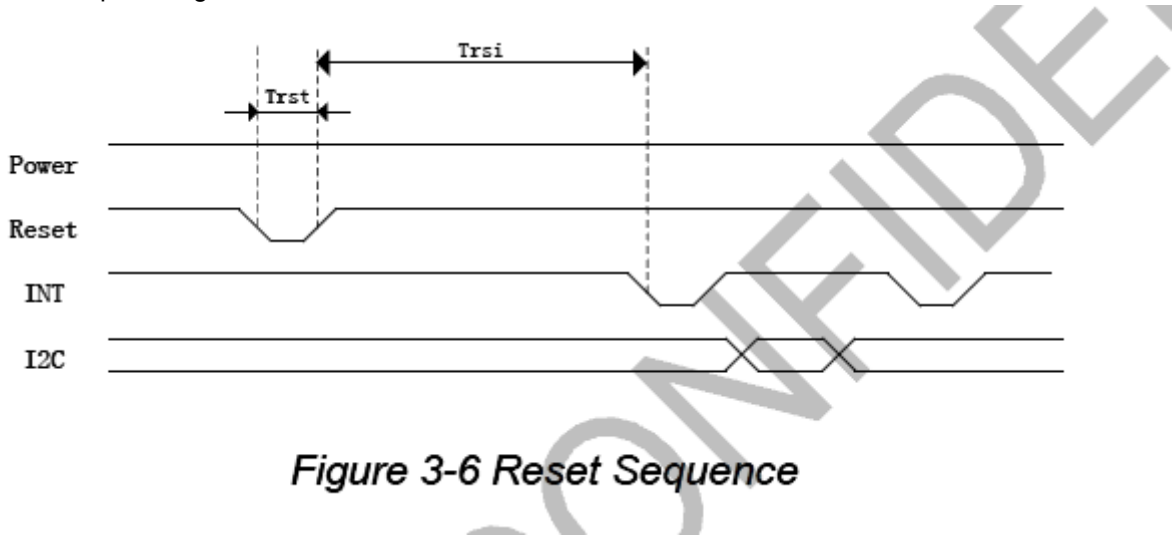


Figure 3-6 Reset Sequence

Table 3-5 Power on/Reset Sequence Parameters

Parameter	Description	Min	Max	Units
Tris	Rise time from 0.1VDD to 0.9VDD	—	5	ms
Tpdt	Time of the voltage of supply being below 0.3V	5	—	ms
Trtp	Time of resetting to be low before powering on	100	—	μs
Tvdr	Reset time after VDD powering on	1	—	ms
Trsi	Time of starting to report point after resetting	—	200	ms
Trst	Reset time	1	—	ms

8.3 I2C Timing

FT5436 supports the I2C interfaces, which can be used by a host processor or other devices.

The I2C is always configured in the Slave mode. The data transfer format is shown in **Figure 2-4**.

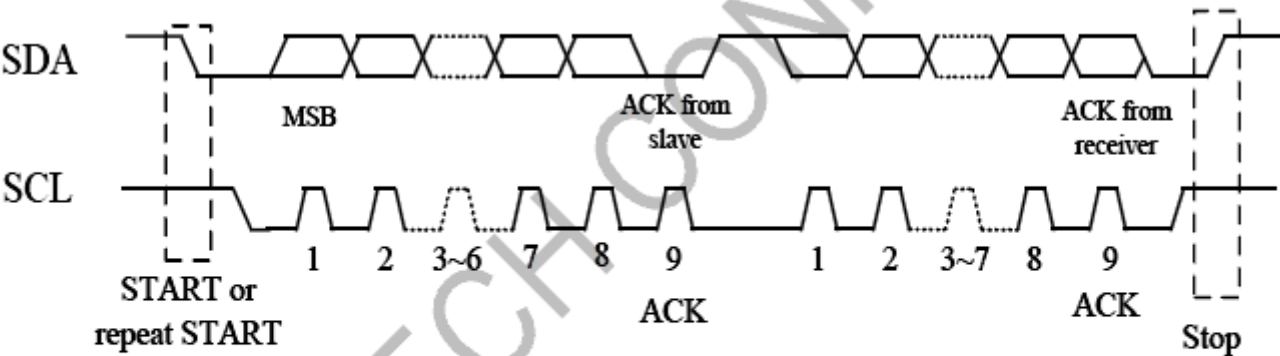


Figure 2-4 I2C Serial Data Transfer Format

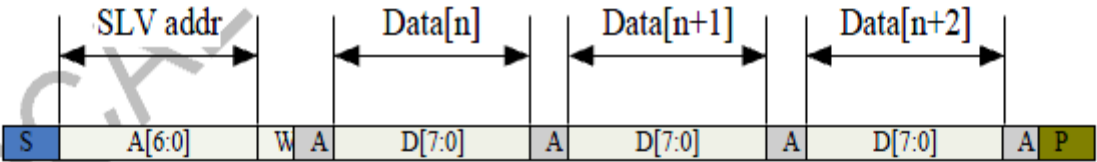


Figure 2-5 I2C master write, slave read

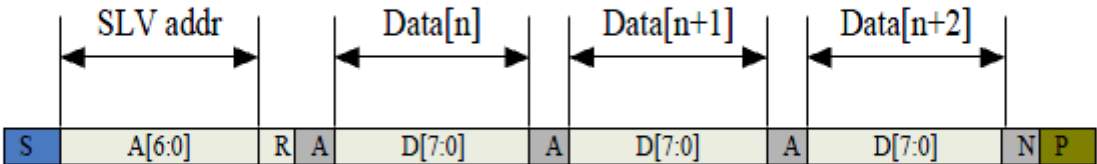


Figure 2-6 I2C master read, slave write

Table 2-1 lists the meanings of the mnemonics used in the above figures.

Table 2-1 Mnemonics Description

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Mnemonics	Description
S	I2C Start or I2C Restart
A[6:0]	Slave address
R/ W	READ/WRITE bit, '1' for read, '0'for write
A(N)	ACK(NACK) bit
P	STOP: the indication of the end of a packet (if this bit is missing, S will indicate the end of the current packet and the beginning of the next packet)

I2C Interface Timing Characteristics is shown in Table 2-2.

Table 2-2 I2C Timing Characteristics

Parameter	Min	Max	Unit
SCL frequency	0	400	KHz
Bus free time between a STOP and START condition	1.3		us
Hold time (repeated) START condition	0.6		us
Data setup time	100		ns
Setup time for a repeated START condition	0.6		us
Setup Time for STOP condition	0.6		us

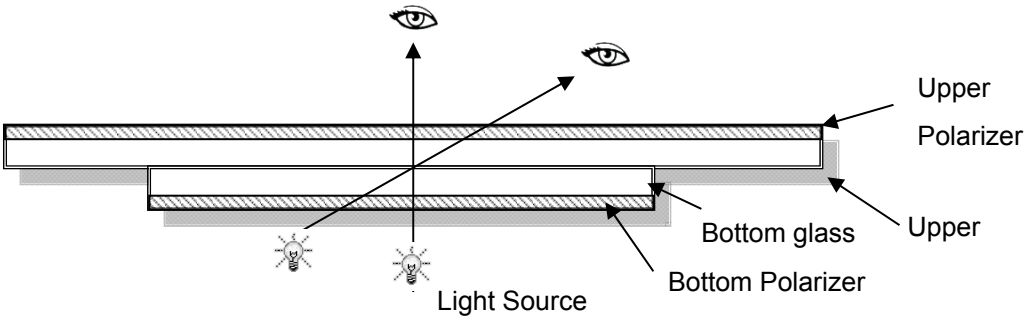
9. LCD Module Out-Going Quality Level

9.1 VISUAL & FUNCTION INSPECTION STANDARD

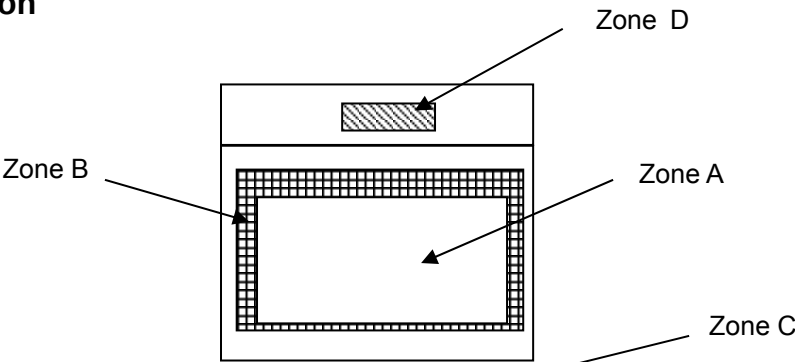
9.1.1 Inspection conditions

Inspection performed under the following conditions is recommended.

- Temperature : 25±5℃
- Humidity : 65%±10%RH
- Viewing Angle : Normal viewing Angle.
- Illumination: Single fluorescent lamp (300 to 700Lux)
- Viewing distance:30-50cm



9.1.2 Definition



- Zone A : Effective Viewing Area(Character or Digit can be seen)
- Zone B : Viewing Area except Zone A
- Zone C : Outside (Zone A+Zone B) which can not be seen after assembly by customer .)
- Zone D : IC Bonding Area

Note:

As a general rule ,visual defects in Zone C can be ignored when it doesn't effect product function or appearance after assembly by customer

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9.1.3 Sampling Plan

According to GB/T 2828.1-2003 ; , normal inspection, Class II

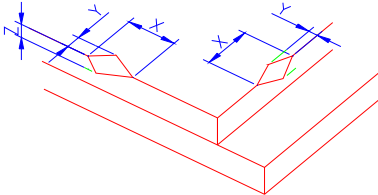
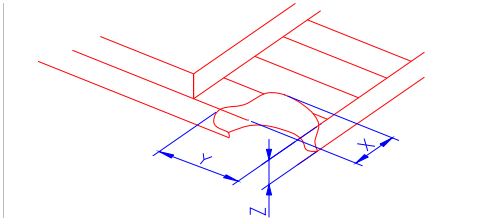
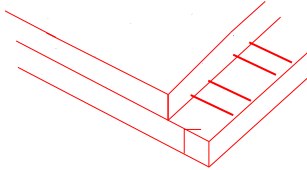
AQL:

Major defect	Minor defect
0.65	1.5

LCD: Liquid Crystal Display , TP: Touch Panel , LCM: Liquid Crystal Module

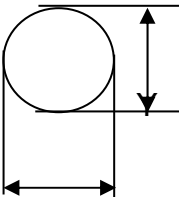
No	Items to be inspected	Criteria	Classification of defects
1	Functional defects	1) No display, Open or miss line 2) Display abnormally, Short 3) Backlight no lighting, abnormal lighting. 4) TP no function	Major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	
4	Color tone	Color unevenness, refer to limited sample	Minor
5	Spot Line defect	Light dot, Dim spot,Polarizer Bubble ; Polarizer accidented spot.	
6	Soldering appearance	Good soldering , Peeling off is not allowed.	
7	LCD/Polarizer/TP	Black/White spot/line, scratch, crack, etc.	

9.1.4 Criteria (Visual)

Number	Items	Criteria(mm)						
1.0 LCD Crack/Broken NOTE: X: Length Y: Width Z: Height L: Length of ITO, T: Height of LCD	(1) The edge of LCD broken	 <table><tr><td>X</td><td>Y</td><td>Z</td></tr><tr><td>≤3.0mm</td><td><Inner border line of the seal</td><td>≤T</td></tr></table>	X	Y	Z	≤3.0mm	<Inner border line of the seal	≤T
	X	Y	Z					
	≤3.0mm	<Inner border line of the seal	≤T					
(2)LCD corner broken	 <table><tr><td>X</td><td>Y</td><td>Z</td></tr><tr><td>≤3.0mm</td><td>≤L</td><td>≤T</td></tr></table>	X	Y	Z	≤3.0mm	≤L	≤T	
X	Y	Z						
≤3.0mm	≤L	≤T						
(3) LCD crack	 Crack Not allowed							

2.0

Spot defect



X

Φ=(X+Y)/2

① light dot (LCD/TP/Polarizer black/white spot , light dot, pinhole, dent, stain)

Zone Size (mm)	Acceptable Qty		
	A	B	C
Φ≤0.10	Ignore		Ignore
0.10<Φ≤0.25	3(distance ≥ 10mm)		
0.25<Φ≤0.3	2		
Φ>0.35	0		

②Dim spot (LCD/TP/Polarizer dim dot, light leakage、 dark spot)

Zone Size (mm)	Acceptable Qty		
	A	B	C
Φ≤0.1	Ignore		Ignore
0.10<Φ≤0.25	3(distance ≥ 10mm)		
0.25<Φ≤0.3	2		
Φ>0.35	0		

③ Polarizer accidented spot

Zone Size (mm)	Acceptable Qty		
	A	B	C
Φ≤0.2	Ignore		Ignore
0.3<Φ≤0.5	2(distance ≥ 10mm)		
Φ>0.5	0		

④Pixel bad points (light dot, Dim dot, color dot)

Zone Size (mm)	Acceptable Qty		
	A	B	C
Φ≤0.1	Ignore		Ignore
0.15<Φ≤0.25	2(distance ≥ 10mm)		
Φ>0.3	0		

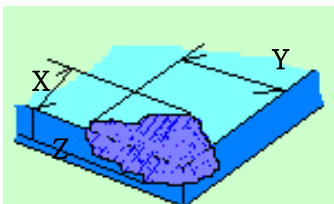
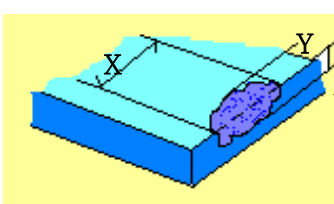
⑤ Polarizer Bubble

Zone Size (mm)	Acceptable Qty		
	A	B	C
Φ≤0.2	Ignore		Ignore
0.3<Φ≤0.4	3(distance ≥ 10mm)		
0.5<Φ≤0.6	2		
0.6<Φ	0		

3.0	Line defect (LCD/TP /Polarizer backlight black/white line, scratch, stain)	Width(mm)	Length(m m)	Acceptable Qty		
				A	B	C
		$\Phi\leq0.05$	Ignore	Ignore		Ignore
		$0.05<W\leq0.06$	$L\leq4.0$	$N\leq3$		
		$0.07<W\leq0.08$	$L\leq3.0$	$N\leq2$		
		$0.08<W$	Define as spot defect			

4.0	Electronic Comp onents SMT	Not allow missing parts, solderless connection, cold solder joint, mis match, The positive and negative polarity opposite
5.0	Display color& B rightness	1. Color: Measuring the color coordinates, The measurement standar d according to the datasheet or samples. 2. Brightness: Measuring the brightness of White screen, The meas urement standard according to the datasheet or Samples.
6.0	LCD Mura	By 5% ND filter invisible.

7.0	CTP Related	CTP Cover sensor accidented black/white spot	<table><tr><td rowspan="2">Size Φ(mm)</td><td colspan="3">Acceptable Qty</td></tr><tr><td>A</td><td>B</td><td>C</td></tr><tr><td>$\Phi \leq 0.1$</td><td colspan="2">Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>$0.1 < \Phi \leq 0.2$</td><td colspan="2">3 (distance ≥ 10mm)</td></tr><tr><td>$0.20 < \Phi \leq 0.25$</td><td colspan="2">2</td></tr><tr><td>$\Phi > 0.3$</td><td colspan="2">0</td></tr></table>				Size Φ (mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.1$	Ignore		Ignore	$0.1 < \Phi \leq 0.2$	3 (distance ≥ 10 mm)		$0.20 < \Phi \leq 0.25$	2		$\Phi > 0.3$	0									
			Size Φ (mm)	Acceptable Qty																														
				A	B	C																												
			$\Phi \leq 0.1$	Ignore		Ignore																												
			$0.1 < \Phi \leq 0.2$	3 (distance ≥ 10 mm)																														
			$0.20 < \Phi \leq 0.25$	2																														
		$\Phi > 0.3$	0																															
		CTP Cover scratch	<table><tr><td rowspan="2">Width(mm)</td><td rowspan="2">Ignore(mm)</td><td colspan="3">Acceptable Qty</td></tr><tr><td>A</td><td>B</td><td>C</td></tr><tr><td>$\Phi \leq 0.05$</td><td>Ignore</td><td colspan="3">Ignore</td></tr><tr><td>$0.05 < W \leq 0.06$</td><td>$L \leq 4.0$</td><td colspan="3">$N \leq 3$</td></tr><tr><td>$0.07 < W \leq 0.08$</td><td>$L \leq 3.0$</td><td colspan="3">$N \leq 2$</td></tr><tr><td>$0.08 < W$</td><td colspan="3">Define as spot defect</td></tr></table>				Width(mm)	Ignore(mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.05$	Ignore	Ignore			$0.05 < W \leq 0.06$	$L \leq 4.0$	$N \leq 3$			$0.07 < W \leq 0.08$	$L \leq 3.0$	$N \leq 2$			$0.08 < W$	Define as spot defect			
			Width(mm)	Ignore(mm)	Acceptable Qty																													
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$0.07 < W \leq 0.08$	$L \leq 3.0$		$N \leq 2$																															
$0.08 < W$	Define as spot defect																																	

		CTP Cover Pinhole/ Lack of ink <table><tr><th> Zone Size (mm) </th><th colspan="2">Acceptable Qty</th></tr><tr><td>$\Phi \leq 0.1$</td><td colspan="2">C</td></tr><tr><td>$0.1 < \Phi \leq 0.2$</td><td colspan="2">Ignore</td></tr><tr><td>$0.25 < \Phi \leq 0.3$</td><td colspan="2">3(distance $\geq 10\text{mm}$)</td></tr><tr><td>$\Phi > 0.35$</td><td colspan="2">2</td></tr><tr><td></td><td colspan="2">0</td></tr></table>	Zone Size (mm)	Acceptable Qty		$\Phi \leq 0.1$	C		$0.1 < \Phi \leq 0.2$	Ignore		$0.25 < \Phi \leq 0.3$	3(distance $\geq 10\text{mm}$)		$\Phi > 0.35$	2			0	
Zone Size (mm)	Acceptable Qty																			
$\Phi \leq 0.1$	C																			
$0.1 < \Phi \leq 0.2$	Ignore																			
$0.25 < \Phi \leq 0.3$	3(distance $\geq 10\text{mm}$)																			
$\Phi > 0.35$	2																			
	0																			
	CTP Bonding bubble/ accident spot <table><tr><th>Size $\Phi(\text{mm})$</th><th colspan="2">Acceptable Qty</th></tr><tr><td>$\Phi \leq 0.1$</td><td>A</td><td>B</td></tr><tr><td>$0.15 < \Phi \leq 0.2$</td><td colspan="2">Ignore</td></tr><tr><td>$0.2 < \Phi \leq 0.25$</td><td colspan="2">3(distance $\geq 10\text{mm}$)</td></tr><tr><td>$\Phi > 0.25$</td><td colspan="2">2</td></tr><tr><td></td><td colspan="2">0</td></tr></table>	Size $\Phi(\text{mm})$	Acceptable Qty		$\Phi \leq 0.1$	A	B	$0.15 < \Phi \leq 0.2$	Ignore		$0.2 < \Phi \leq 0.25$	3(distance $\geq 10\text{mm}$)		$\Phi > 0.25$	2			0		
Size $\Phi(\text{mm})$	Acceptable Qty																			
$\Phi \leq 0.1$	A	B																		
$0.15 < \Phi \leq 0.2$	Ignore																			
$0.2 < \Phi \leq 0.25$	3(distance $\geq 10\text{mm}$)																			
$\Phi > 0.25$	2																			
	0																			
	Assembly deflection	beyond the edge of backlight $\leq 0.2\text{mm}$																		
	TP cover broken X : length Y : width Z : height <table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>$X \leq 0.5\text{mm}$</td><td>$Y \leq 0.5\text{mm}$</td><td>$Z < \text{cover thickness}$</td></tr></table> * Circuitry broken is not allowed.	X	Y	Z	$X \leq 0.5\text{mm}$	$Y \leq 0.5\text{mm}$	$Z < \text{cover thickness}$													
X	Y	Z																		
$X \leq 0.5\text{mm}$	$Y \leq 0.5\text{mm}$	$Z < \text{cover thickness}$																		
	TP cover broken X : length Y : width Z : height <table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>$X \leq 0.3\text{mm}$</td><td>$Y \leq 0.3\text{mm}$</td><td>$Z < \text{LCD thickness}$</td></tr></table> * Circuitry broken is not allowed.	X	Y	Z	$X \leq 0.3\text{mm}$	$Y \leq 0.3\text{mm}$	$Z < \text{LCD thickness}$													
X	Y	Z																		
$X \leq 0.3\text{mm}$	$Y \leq 0.3\text{mm}$	$Z < \text{LCD thickness}$																		

Criteria (functional items)

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常备库存 Stock For Sale	长期供货 Long Time supply	支持小量 NO MOQ	品种齐全 In Full Range	

Number	Items	Criteria (mm)
1	No display	Not allowed
2	Missing segment	Not allowed
3	Short	Not allowed
4	Backlight no lighting	Not allowed
5	TP no function	Not allowed

10. Reliability Test Result

Item	Condition	Inspection after test
High Temperature Operating	85°C,96H	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD; 2.Non-display; 3.Missing segments/line; 4.Glass crack; 5.Current IDD is twice higher than initial value.
Low Temperature Operating	-30°C, 96HR	
High Temperature Storage	85°C, 96HR	
Low Temperature Storage	-30°C, 96HR	
High Temperature & High	+60°C, 90% RH ,96 hours.	
Thermal Shock (Non-operation)	-30°C,30 min ↔ 85°C,30 min, Change time:5min 20CYC.	
ESD test	C=150pF, R=330,5points/panel Air:±8KV, 5times; Contact:±6KV, 5 times; (Environment: 15°C~35°C, 30%~60%).	
Vibration (Non-operation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total) (Package condition).	
Box Drop Test	1 Corner 3 Edges 6 faces,80cm(MEDIUM BOX)	

Remark:

- 1.The test samples should be applied to only one test item.
- 2.Sample size for each test item is 5~10pcs.
- 3.For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5.Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

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常备库存 Stock For Sale	长期供货 Long Time supply	支持小量 NO MOQ	品种齐全 In Full Range	

11. Cautions and Handling Precautions

11.1 Handling and Operating the Module

- (1) When the module is assembled, it should be attached to the system firmly.
Do not warp or twist the module during assembly work.
- (2) Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
- (3) Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.
- (4) Do not allow drops of water or chemicals to remain on the display surface.
If you have the droplets for a long time, staining and discoloration may occur.
- (5) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (6) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane.
Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.
- (8) Protect the module from static; it may cause damage to the CMOS ICs.
- (9) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (10) Do not disassemble the module.
- (11) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (12) Pins of I/F connector shall not be touched directly with bare hands.
- (13) Do not connect, disconnect the module in the "Power ON" condition.
- (14) Power supply should always be turned on/off by the item 6.1 Power On Sequence & 6.2 Power Off Sequence

11.2 Storage and Transportation.

- (1) Do not leave the panel in high temperature, and high humidity for a long time.
It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%
- (2) Do not store the TFT-LCD module in direct sunlight.
- (3) The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.
- (4) It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module.
In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.
- (5) This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.

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常备库存 Stock For Sale	长期供货 Long Time supply	支持小量 NO MOQ	品种齐全 In Full Range	

12. Packing

----TBD-----

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